



MSLIN98-005

November 7, 2001

TO: Commissioner of Patents and Trademarks
Washington, D.C. 20231

CC: Examiner: Tammara Peyton
Art Unit#: 2182

FROM: George O. Saile, Reg. No. 19,572
20 McIntosh Drive
Poughkeepsie, N.Y. 12603

SUBJECT: Serial #: 09/246,303
File Date: 2/28/99
Inventors: Mou-Shiung Lin
Title: Integrated Circuit Module has Common Function Known Good
Integrated Circuit Die with Multiple Selectable Functions

Dear Sir:

In response to the Notice of Allowability dated 8/24/01, we are submitting the Issue Fee without the requested Formal Drawings. On October 23, 2001 examiner Tammara Peyton indicated that the drawings were requested in error.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231 on November 7, 2001.

Signature


Stephen B. Ackerman, Reg. No. 37,761

Date: November 7, 2001

OK to Enter



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A handwritten signature in black ink, appearing to be "S. Ackerman", written over a horizontal line.

Stephen B. Ackerman, Reg. No. 37,761

Date: November 7, 2001

OK to Enter

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It is requested that should there be further problems with the drawings, call the undersigned Attorney at (845) 452-5863.

Respectfully submitted,



Stephen B. Ackerman, Reg. No. 37,761

Enclosures

OK to Enter